

U.S. PTO  
10/087350  
03/01/02

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10087350	03/01/2002	711-714	102	2185	Follensee

APPLICANTS: McNeil Donald;

2151  
2127

\*\*CONTINUING DATA VERIFIED:  
THIS APPLN CLAIMS BENEFIT OF 60/277,545 03/22/2001

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FOREIGN APPLICATIONS VERIFIED:

DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO
1.000 claims <input type="checkbox"/> yes <input type="checkbox"/> no	35 U.S.C. 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no	803-01
Verifier and Acknowledged Examiner's initials		U.S. DEPT. OF COMM/PAT. & TM-PRO-40016 (Rev. 10-8-01)
TITLE : Ultra-modular processor in lattice topology		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		Primary Examiner	
Amount Due	Date Paid	Total Claims	Print Claim for O.G
		DRAWING	
		Sheets Drawg.	Figs. Drawg.
		Print Fig.	
TERMINAL DISCLAIMER		Application Examiner	
PREPARED FOR ISSUE			
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